

## **IBM previews performance of System x server based on next-generation Dual Core Intel processor**

*IBM System x3650 delivers recording-setting benchmark scores for Intel processor-based servers*

May 23, 2006 ... IBM® System x™ servers based on Intel® Core™ Microarchitecture will deliver new levels of performance. The IBM System x3650 that uses the latest and fastest Intel dual-core processor is part of the System x server line planned for formal debut in June 2006. Based on new benchmark results, this two-socket server delivers dual-core power and excellent server function using the latest Dual Core Intel Xeon™ Processors. (1)

### **Microsoft Exchange MMB3 Benchmark**

The x3650 achieved 11,600 MMB3—the highest score achieved to date by an Intel processor-based server—on the Microsoft® Exchange MAPI Messaging Benchmark 3 (MMB3). The x3650 achieved this score using two Dual Core Intel Xeon Processor 5160 (3.0GHz, 2MB L2 cache per core, and 1333 MHz front-side bus) and Microsoft Windows® Server 2003 Enterprise Edition.

Results are current as of May 23, 2006. View at MMB3 results at [www.microsoft.com/exchange/evaluation/performance/](http://www.microsoft.com/exchange/evaluation/performance/)

### **SPECweb2005 Benchmark**

The x3650 server achieved a supermetric score of 9,182—the highest score to date for an Intel processor-based server and the best score achieved by a 2-processor system. The supermetric score is derived from the submetric scores of the three workloads measured:

- SPECweb2005\_Banking - 15,000 simultaneous sessions
- SPECweb2005\_Ecommerce - 13,000 simultaneous sessions
- SPECweb2005\_Support - 8,800 simultaneous sessions

The x3650 achieved these results using two Dual Core Intel Xeon Processor 5160 (3.0GHz, 4MB L2 cache per core, and 1333 MHz front-side bus). The x3650 also used 24GB of memory, 64-bit Red Hat® Enterprise Linux 4 AS Update 3 operating system, SunJava 1.5.0-06 Java Virtual Machine, 64-bit Accoria Rock Web Server 1.3.3 HTTPS software, and Apache Tomcat 5.5.9.

Results are current as of May 23, 2006, and have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all SPECweb2005 published results at [www.spec.org](http://www.spec.org).

(1) Planned availability for the x3650 models using the Dual Core Intel Xeon Processor 5160 (3.0GHz, 2MB L2 cache per core, and 1333 MHz front-side bus) is August 25, 2006.

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